AMENDMENTS TO THE SPECIFICATION

Please amend the specification beginning on page 9, line 26 through page 10, line 4 as follows:

Figure 49 is a flowchart showing steps taken in compiling source code into byte code; [[and]]

Figure 50 is a flowchart showing steps taken in translating byte code into processor-specific object code and loading the processor-specific object code into a corresponding processor type[[.]]; and

Figure 51 is a block diagram illustrating a processing element having a main processor and a plurality of secondary processors sharing a system memory.